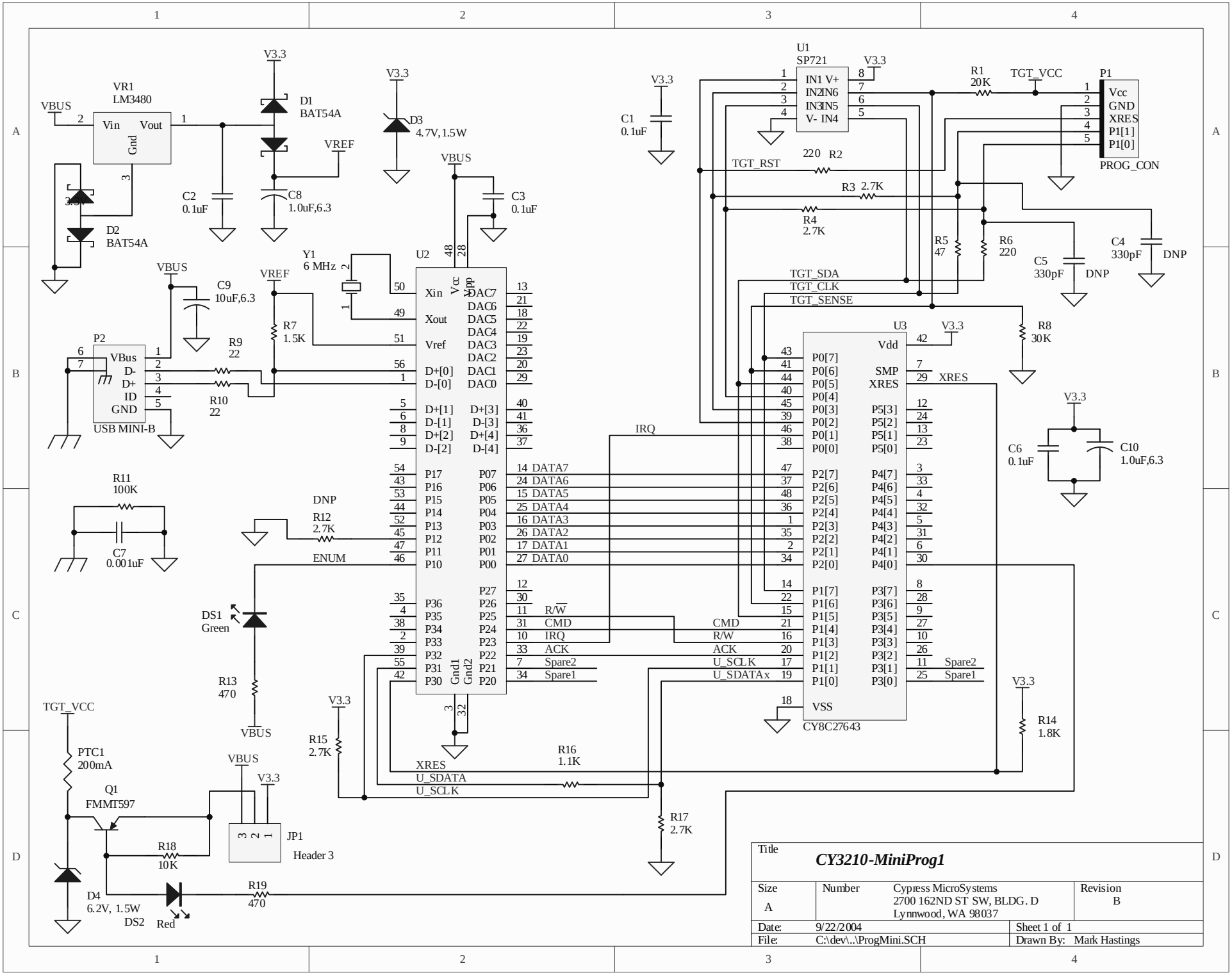
VR1
LM3480
Vin
Vout
Gnd
VBUS
V3.3
D1
BAT54A
D2
BAT54A
VREF
C2
0.1uF
C8
1.0uF,6.3
D3
4.7V,1.5W
C3
0.1uF
C1
0.1uF
U1
SP721
IN1 V+
IN2 IN6
IN3 IN5
V- IN4
R1
20K
TGT_VCC
P1
Vcc
GND
XRES
P1[1]
P1[0]
PROG_CON
TGT_RST
R2
220
R3
2.7K
R4
2.7K
R5
47
R6
220
C5
330pF
DNP
C4
330pF
DNP
TGT_SDA
TGT_CLK
TGT_SENSE
Y1
6 MHz
U2
Xin
Xout
Vref
Vcc
Vpp
D+[0]
D-[0]
D+[1]
D-[1]
D+[2]
D-[2]
DAC7
DAC6
DAC5
DAC4
DAC3
DAC2
DAC1
DAC0
D+[3]
D-[3]
D+[4]
D-[4]
P17
P16
P15
P14
P13
P12
P11
P10
P07
P06
P05
P04
P03
P02
P01
P00
P27
P26
P25
P24
P23
P22
P21
P20
P36
P35
P34
P33
P32
P31
P30
Gnd1
Gnd2
DATA7
DATA6
DATA5
DATA4
DATA3
DATA2
DATA1
DATA0
R/W
CMD
IRQ
ACK
Spare2
Spare1
ENUM
DNP
R12
2.7K
C9
10uF,6.3
R7
1.5K
R9
22
R10
22
P2
VBus
D-
D+
ID
GND
USB MINI-B
R11
100K
C7
0.001uF
DS1
Green
R13
470
TGT_VCC
PTC1
200mA
Q1
FMMT597
R18
10K
D4
6.2V, 1.5W
DS2
Red
R19
470
JP1
Header 3
R15
2.7K
R16
1.1K
XRES
U_SDATA
U_SCLK
R17
2.7K
U3
CY8C27643
Vdd
SMP
XRES
P0[7]
P0[6]
P0[5]
P0[4]
P0[3]
P0[2]
P0[1]
P0[0]
P5[3]
P5[2]
P5[1]
P5[0]
P2[7]
P2[6]
P2[5]
P2[4]
P2[3]
P2[2]
P2[1]
P2[0]
P4[7]
P4[6]
P4[5]
P4[4]
P4[3]
P4[2]
P4[1]
P4[0]
P1[7]
P1[6]
P1[5]
P1[4]
P1[3]
P1[2]
P1[1]
P1[0]
P3[7]
P3[6]
P3[5]
P3[4]
P3[3]
P3[2]
P3[1]
P3[0]
VSS
U_SDATAx
R8
30K
C6
0.1uF
C10
1.0uF,6.3
R14
1.8K
Title
CY3210-MiniProg1
Size
A
Number
Cypress MicroSystems
2700 162ND ST SW, BLDG. D
Lynnwood, WA 98037
Revision
B
Date: 9/22/2004
Sheet 1 of 1
File: C:\dev\..\ProgMini.SCH
Drawn By: Mark Hastings